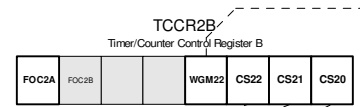
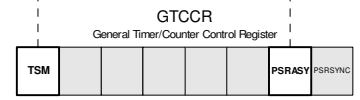
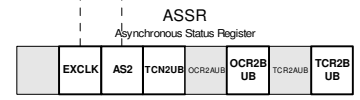
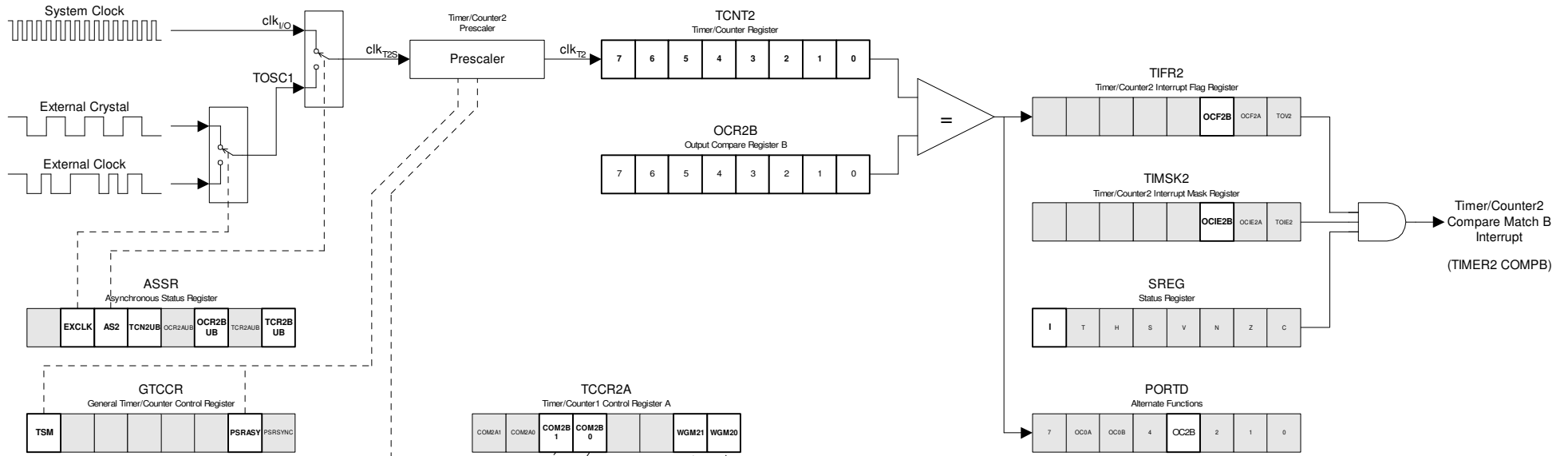
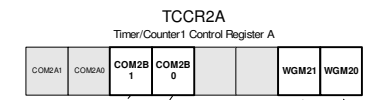


Timer/Counter2 Compare Match B

ATmega48/88/168/328 etc.



CS22	CS21	CS20	Clock Select
0	0	0	Timer/Counter stopped
0	0	1	clk_{T2S}
0	1	0	$clk_{T2S} / 8$
0	1	1	$clk_{T2S} / 32$
1	0	0	$clk_{T2S} / 64$
1	0	1	$clk_{T2S} / 128$
1	1	0	$clk_{T2S} / 256$
1	1	1	$clk_{T2S} / 1024$



Compare Output Mode

COM2B1	COM2B0	Mode
0	0	Normal Port Operation, OC2B disconnected
0	1	Toggle OC2B on Compare Match
1	0	Clear OC2B on Compare Match
1	1	Set OC2B on Compare Match

Fast PWM Mode
(WGM modes 3 and 7)

COM2B1	COM2B0	Mode
0	0	Normal Port Operation, OC2B disconnected
0	1	Mode 7: Toggle OC2B on Compare Match All other modes: Normal Port Operation, OC2B disconnected
1	0	Clear OC2B on Compare Match, set OC2B at BOTTOM (non-inverting mode)
1	1	Set OC2B on Compare Match, clear OC2B at BOTTOM (inverting mode)

Phase Correct PWM Mode
(WGM modes 1 and 5)

COM2B1	COM2B0	Mode
0	0	Normal Port Operation, OC2B disconnected
0	1	Mode 5: Toggle OC2B on Compare Match All other modes: Normal Port Operation, OC2B disconnected
1	0	Clear OC2B on Compare Match when up-counting. Set OC2B on Compare Match when down-counting.
1	1	Set OC2B on Compare Match when up-counting. Clear OC2B on Compare Match when down-counting.

Waveform Generation Mode

Mode	WGM22	WGM21	WGM20	Timer / Counter Mode of Operation	TOP	Update of OCR2B at	TOV Flag Set on
0	0	0	0	Normal	0xFF	Immediate	0xFF
1	0	0	1	PWM, Phase Correct	TOP	0x00	0x00
2	0	1	0	CTC	OCR2A	Immediate	0xFF
3	0	1	1	Fast PWM	0xFF	BOTTOM	0xFF
4	1	0	0	Reserved			
5	1	0	1	PWM, Phase Correct	OCR2A	TOP	0x00
6	1	1	0	Reserved			
7	1	1	1	Fast PWM	OCR2A	BOTTOM	TOP

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