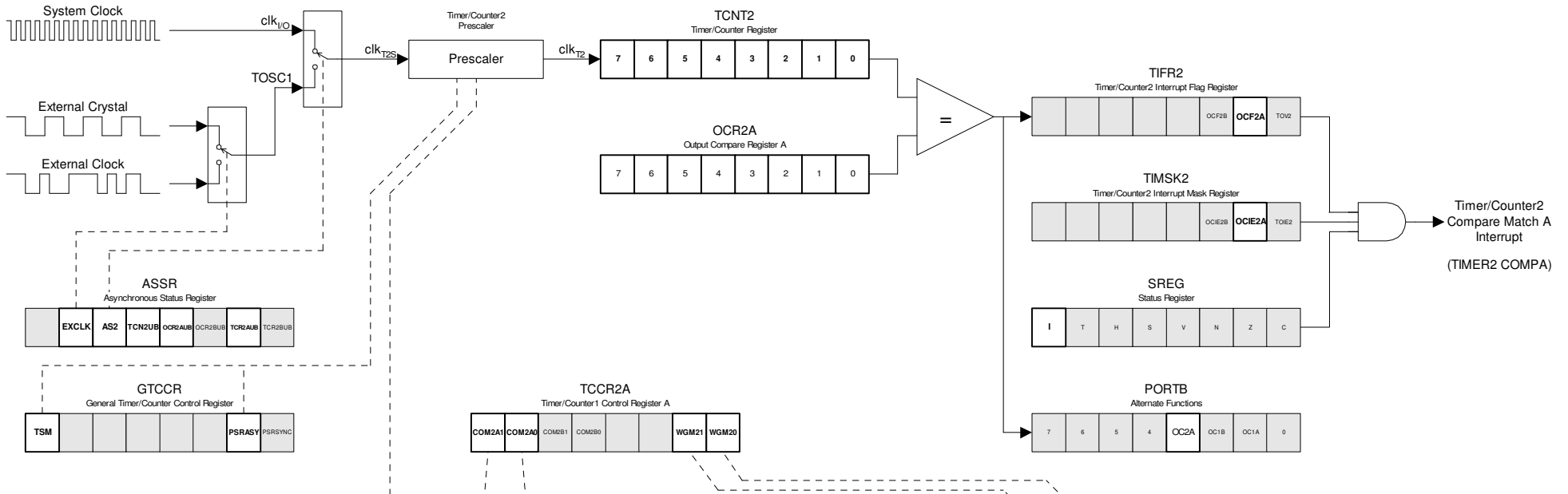


# Timer/Counter2 Compare Match A

## ATmega48/88/168/328 etc.



CS22	CS21	CS20	Clock Select
0	0	0	Timer/Counter stopped
0	0	1	$clk_{T2S}$
0	1	0	$clk_{T2S} / 8$
0	1	1	$clk_{T2S} / 32$
1	0	0	$clk_{T2S} / 64$
1	0	1	$clk_{T2S} / 128$
1	1	0	$clk_{T2S} / 256$
1	1	1	$clk_{T2S} / 1024$

COM2A1	COM2A0	Compare Output Mode
0	0	non-PWM Mode (WGM modes 0 and 2) Normal Port Operation, OC2A disconnected
0	1	Toggle OC2A on Compare Match
1	0	Clear OC2A on Compare Match
1	1	Set OC2A on Compare Match

COM2A1	COM2A0	Fast PWM Mode (WGM modes 3 and 7)
0	0	Normal Port Operation, OC2A disconnected
0	1	Mode 7: Toggle OC2A on Compare Match All other modes: Normal Port Operation, OC2A disconnected
1	0	Clear OC2A on Compare Match, set OC2A at BOTTOM (non-inverting mode)
1	1	Set OC2A on Compare Match, clear OC2A at BOTTOM (inverting mode)

COM2A1	COM2A0	Phase Correct PWM Mode (WGM modes 1 and 5)
0	0	Normal Port Operation, OC2A disconnected
0	1	Mode 5: Toggle OC2A on Compare Match All other modes: Normal Port Operation, OC2A disconnected
1	0	Clear OC2A on Compare Match when up-counting. Set OC2A on Compare Match when down-counting.
1	1	Set OC2A on Compare Match when up-counting. Clear OC2A on Compare Match when down-counting.

Mode	WGM22	WGM21	WGM20	Timer / Counter Mode of Operation	TOP	Update of OCR2A at	TOV Flag Set on
0	0	0	0	Normal	0xFF	Immediate	0xFF
1	0	0	1	PWM, Phase Correct	0xFF	TOP	0x00
2	0	1	0	CTC	OCR2A	Immediate	0xFF
3	0	1	1	Fast PWM	0xFF	BOTTOM	0xFF
4	1	0	0	Reserved			
5	1	0	1	PWM, Phase Correct	OCR2A	TOP	0x00
6	1	1	0	Reserved			
7	1	1	1	Fast PWM	OCR2A	BOTTOM	TOP

This work is licensed under the Creative Commons Attribution-ShareAlike License. To view a copy of this license, visit <http://creativecommons.org/licenses/by-sa/3.0/>